



70550

**PCI 32 bit P&P
1P (ECP/EPP 32FIFO)**

Installation Guide

◆ Description

The 70550 3V 32Bit PCI bus Plug & Play 1P I/O card is equipped with one ECP/EPP/SPP/BPP IEEE 1284c CENTRONICS Parallel Interface. The 4008A3V include one 25 pin female connector for peripherals' units with IEEE 1284C CENTRONICS Parallel Interface.

◆ Hardware Specification

Parallel Port:

Parallel Chipset: SUN1888, Hardware FIFO are 32Byte FIFO

Compatible chips: with ACC3203, ST-78C36, and HT6535.

IEEE standard: ECP/EPP/SPP/BPP (PS-II) Interface

Max. Speed: Up to 21.4MB/Sec

◆ Hardware Installation

- Turn off the power of your computer
- Plug the 70550 Parallel Adapter into the PCI bus of your computer
- Connect the peripheral (Printer, Modem, Scanner... etc.) to this adapter
- Turn on the power of your computer

PCI device listing display (for example...)

Bus No	Device No	Func No	Vendor ID	Device ID	Device Class	IRQ
0	7	1	8086	7010	IDE Controller	14
0	13	0	1409	7268	Simple COMM. Controller	10

When Vendor ID, Device ID, Device Class or IRQ aren't displayed or an error occurs, please change the PCI slot.

After correct hardware installation you can start installing the software from the driver CD.

◆ Pin Assignments

Parallel 25 Pin female connector pin assignments:

Parallel 25 Pin female connector:					
Pin	Signal	Pin	Signal	Pin	Signal
1	STROBE	10	ACKNOWLEDGE	19	GROUND
2	DATA 0	11	BUSY	20	GROUND
3	DATA 1	12	PAPER EMPTY	21	GROUND
4	DATA 2	13	SELECT	22	GROUND
5	DATA 3	14	AUTO FEED	23	GROUND
6	DATA 4	15	ERROR	24	GROUND
7	DATA 5	16	INIT	25	GROUND
8	DATA 6	17	SELECT INPUT	26	GROUND
9	DATA 7	18	GROUND		

◆ Technical Specification

Compatibility:	PCI 32Bit card for 486/586/Pentium/Pentium-II/ P-III/P4/C5/K6
Operating system:	WIN9.x/NT/2000/XP, LINUX, DOS
Chips:	1 x SUN1888
Connections:	Connector parallel 1 x DB 25 f
PCB:	2 Layer / 120 x 63 mm

◆ Support

Support pure 3 volt motherboard only



This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operations.