



70555

**PCI 32 bit P&P
2S (16C550 16FIFO)**

Installation Guide

◆ Description

The 70555 3V 32Bit PCI bus Plug & Play 2S I/O card is equipped with two High-Speed RS-232.V24 Standard Serial-Interface, The serial port include 16 Byte FIFO, The speed Up to 460Kb/Sec. The 4036A3V include two 9 pin male connector for attaching peripheral's unit with Serial RS-232 Standard Interface.

◆ Hardware Specification

Serial Port:

UART Chipset: SUN1889, Hardware FIFO is 16Byte FIFO,
Compatible chips: with 16C550 and 16C450.

IEEE standard: RS-232 Interface

Driver Chipset: TI-75232, speed up to 100Kb/Sec, length 300-500M.

Max. Speed: Up to 460Kb/Sec (HYPER Terminal program)

◆ Hardware Installation

- Turn off the power of your computer
- Plug the 70555 Serial Adapter to the PCI bus of your computer
(Connect the peripheral (Printer, Modem, Scanner... etc.) to this adapter
(Turn on the power of your computer

PCI device listing display (for example...)

Bus No	Device No	Func No	Vendor ID	Device ID	Device Class	IRQ
0	7	1	8086	7010	IDE Controller	14
0	13	0	1409	7168	Simple COMM. Controller	10

When Vendor ID, Device ID, Device Class or IRQ aren't displayed or an error occurs, please change the PCI slot.

After correct hardware installation you can start installing the software from the driver CD.

◆ Pin Assignments

Serial 9 Pin male connector pin assignments:

Serial 9 Pin male connector (S1 and S2):							
Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	CD	4	DTR	6	DSR	8	CTS
2	RD	5	GROUND	7	RTS	9	RI
3	TD						

◆ Technical Specification

Compatibility:	PCI 32Bit card for 486/586/Pentium/Pentium-II/ P-III/P4/C5/K6
Operating system:	WIN 9.x/NT/2000/XP, LINUX, DOS
Chips:	1 x SUN1889
Connections:	2 x DB 9 m connector serial
PCB:	2 Layer / 120 x 76 mm

◆ Support

Support pure 3 volt motherboard only



This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operations.