

Phoenix BIOS

Code	POST Routine Description
02	Verify real Mode
03	Disable Non-Maskable Interrupt(NMI)
04	Get CPU type
06	initialize system hardware
07	Disable shadow and execute code from the ROM
08	Initialize chipset with initial POST values
09	Set IN POST flag
0A	Initialize CPU registers
0B	Enable CPU cache
0C	Initialize caches to initial POST values
0E	Initialize I/O component
0F	Initialize the local bus IDE
10	Initialize Power Management
11	Load alternate registers with initial POST values
12	Restore CPU control word during warm boot
13	Initialize PCI Bus Mastering devices
14	Initialize keyboard controller
16	1-2-2-3 BIOS ROM checksum
17	Initialize cache before memory Auto size
18	8254 timer initialization
1A	8237 DMA controller initialization
1C	Reset Programmable Interrupt Controller
20	1-3-1-1 Test DRAM refresh
22	1-3-1-3 Test 8742 Keyboard Controller
24	Set ES segment register to 4 GB
28	Auto size DRAM
29	Initialize POST Memory Manager
2A	Clear 512kB base RAM
2C	1-3-4-1 RAM failure on address line xxxx*
2E	1-3-4-3 RAM failure on data bitsxxxx* of low byte of memory bus
2F	Enable cache before system BIOS shadow
32	Test CPU bus-clock frequency
33	Initialize Phoenix Dispatch Manager
36	Warm start shut down
38	Shadow system BIOS ROM

- 3A Auto size cache
- 3C Advanced configuration of chipset registers
- 3D Load alternate registers with CMOS values
- 41 Initialize extended memory for RomPilot
- 42 Initialixe interrupt vectors
- 45 POST device initialization